

Abstracts

Highly accurate spurious-free integrated VCO resonator in a GSM transceiver using circuit-package co-design

Jenshan Lin, Ran-Hong Yan, R.C. Frye, P.R. Smith and Y.L. Low. "Highly accurate spurious-free integrated VCO resonator in a GSM transceiver using circuit-package co-design." 1998 Radio Frequency Integrated Circuits (RFIC) Symposium 98. (1998 [RFIC]): 123-126.

We report an integrated VCO in a GSM transceiver with less than 1% frequency error in the first design and spurious-free frequency response. The first-design success with high accuracy is due to considering the package as part of the circuit design and included in optimization. The spurious-free frequency response is achieved by carefully designing a high-Q (37.5 at 1 GHz) inductor and a co-planar stripline in the VCO resonator.

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